

Modeling Memory Faults in Signature and Authenticated Encryption Schemes

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joint work with **Marc Fischlin (TU Darmstadt)**

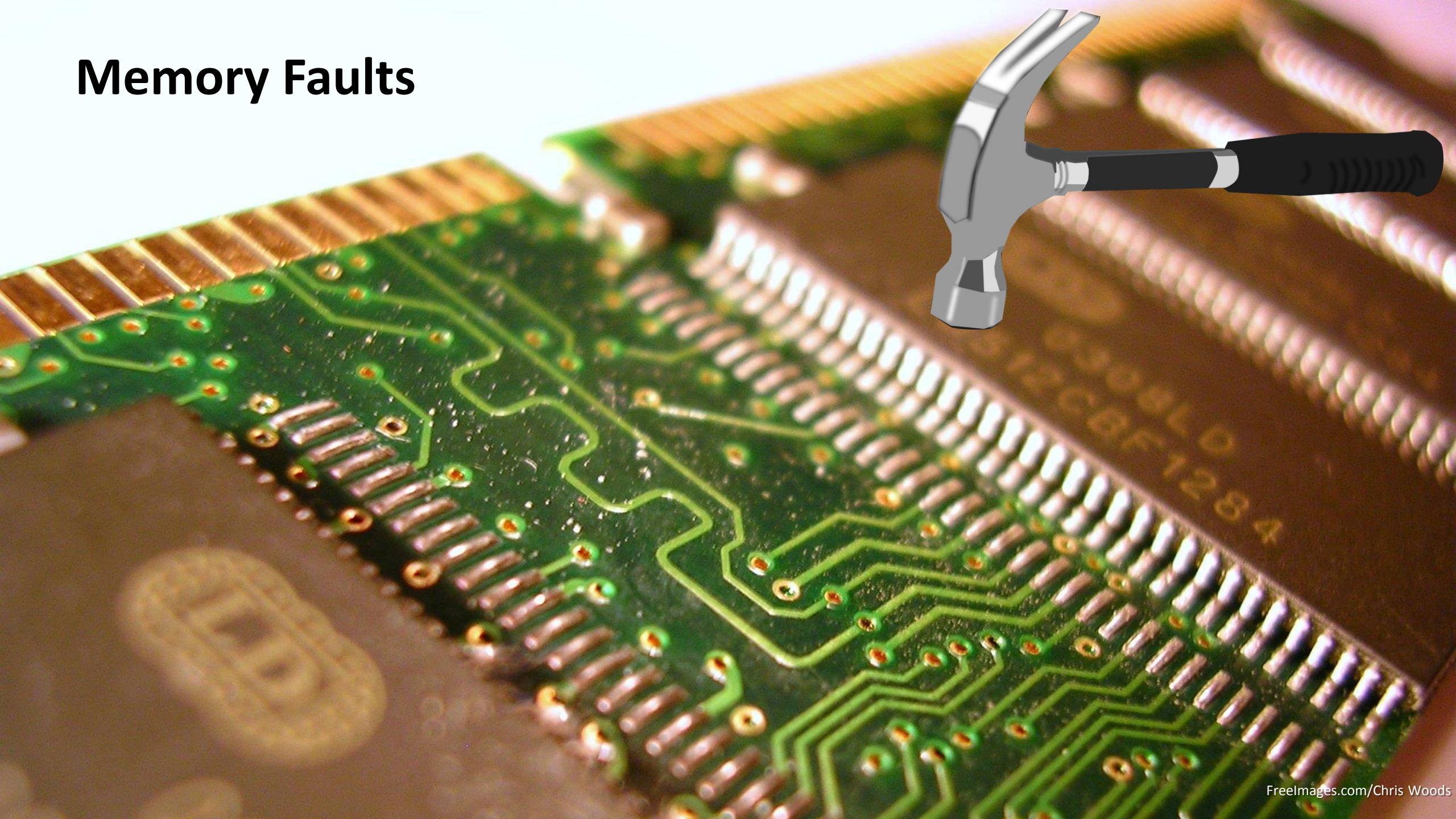
ETH zürich

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RSA Conference 2020
San Francisco
February 25, 2020

Memory Faults



What About the Code?

```
static Private $host;
static Private $username;
static Private $password;
static Private $database;
static Private $charset;

public Public $link = null;

function connect()
{
    $link = mysql_connect($self::$host, $self::$username, $self::$password);
    if (!$link)
    {
        throw new MySQLException("Cannot connect to database");
    }
}
```

The Cryptographic Perspective

Deterministic ECDSA

```
Signdet-ECDSA(sk, m)  
  r ← Hash(sk, m)  
  R ← f(rG) mod q  
  s ← (H(m) + sk R) / r mod q  
  return (R, s)
```

What about faults?

Signature security (EUF-CMA)

Expt_{S,A}^{EUF-CMA}(1^λ):

- 1 $(sk, pk) \xleftarrow{\$} \text{KGen}(1^\lambda)$
- 2 $Q \leftarrow \emptyset$
- 3 $(m^*, \sigma^*) \xleftarrow{\$} \mathcal{A}^{\mathcal{O}_{\text{Sign}}}(1^\lambda, pk)$
- 4 return 1 iff $(m^*, *) \notin Q$
and $\text{Verify}(pk, m^*, \sigma^*) = 1$

$\mathcal{O}_{\text{Sign}}(m)$:

- 5 $\sigma \xleftarrow{\$} \text{Sign}(sk, m)$
- 6 $Q \leftarrow Q \cup \{(m, \sigma)\}$
- 7 return σ

Models Matter

- Deterministic ECDSA (& co.) succumb to rowhammer-style faults

[PSSLR @ IEEE EuroS&P 2018]

$$(R_0, s_0) : \quad H(m) + sk R_0 = \text{Hash}(sk, m) s_0$$

$$(R', s') : \quad H(m) + sk R' = \text{Hash}(sk, m) s'$$

$$sk = H(m) / ((R_0 - R') s_0 / (s_0 - s') - R_0)$$

- We know for long that faults can have devastating effects on crypto operations at software level [BDL @ Eurocrypt 1997]
- But how to assess fault *resilience* in provable-security manner?

Prior Work

- Faults in circuits
[IPSW06]
- Tailored provable-security models (e.g., for RSA)
[CM09, BDFGTZ14, FGTLZ12]
- Related-key attack (RKA) security
[BK04, GLMMR04]
- Hedged randomness in Fiat-Shamir-type signatures under faults
[AOTZ19]

A Generic Framework for Fault Resilience in Security Models

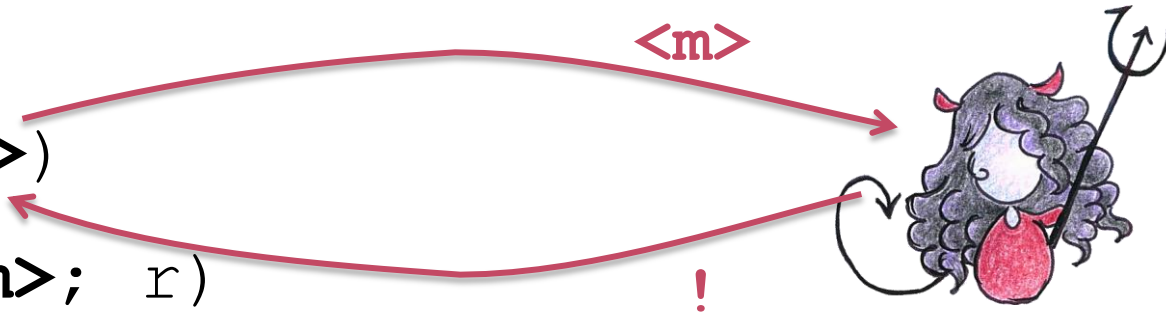
Modeling Fault Resilience

Sign_{dr}(sk, m)

$r \leftarrow \text{Hash}(sk, \langle m \rangle)$

$s \leftarrow \text{Sign}_r(sk, \langle m \rangle; r)$

return s



- **augmented code**, indicating faultable memory variables
- **callbacks** to adversary: may change values of variable readings

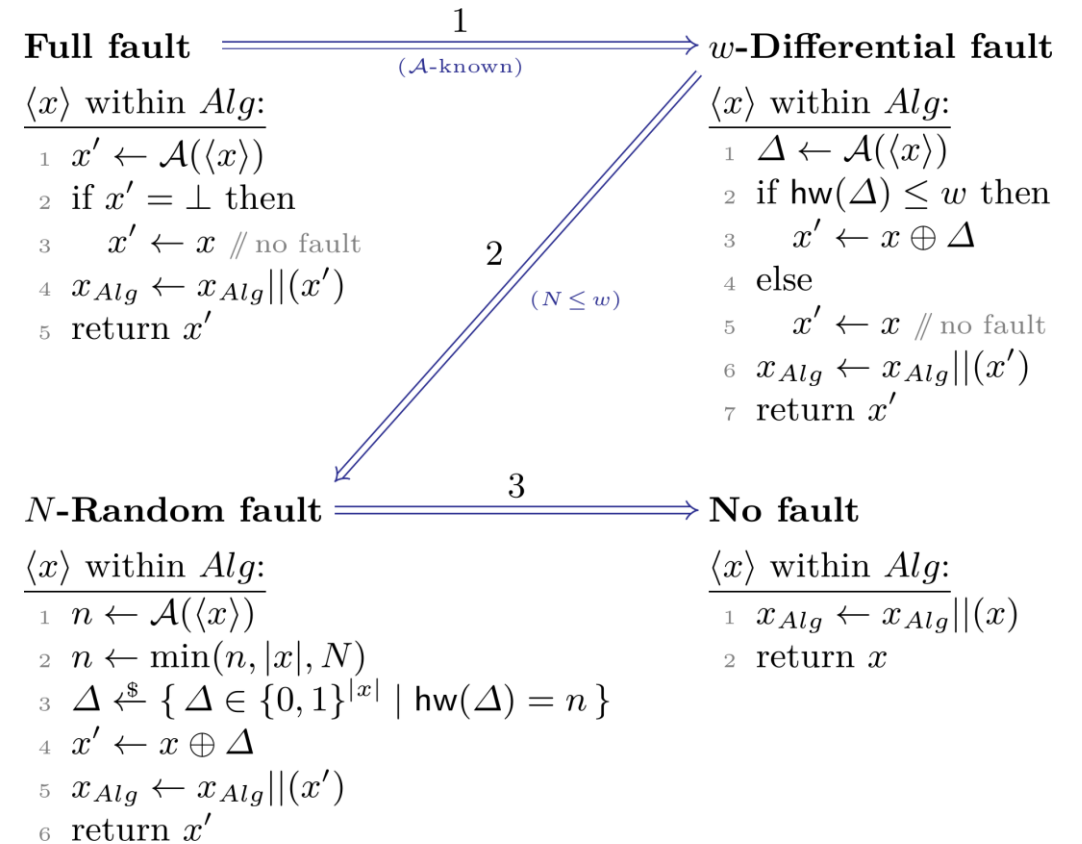
drawing by Giorgia Azzurra Marson

Generic Fault Types

Flexible callbacks

- Full faults
adversary controls variable completely
- Differential faults
adversary can flip w selected bits
- Random faults
adversary can flip N random bits
- No fault
(baseline)

Forming a hierarchy



Fault Resilience for Signatures

Augmenting Signature Security

frEUF-CMA: Fault-resilience unforgeability

Sign_{dr}(sk, **m**)

$r \leftarrow \text{Hash}(sk, \langle \mathbf{m} \rangle)$

$s \leftarrow \text{Sign}_r(sk, \langle \mathbf{m} \rangle; r)$

return **s**

- Essential question:

Which message did the signer sign?

= Which (m,s) is trivially learned?

- Answer: the message **m** (among all appearing in Sign) verifying with **s**
- If there's two such **m** → confusion
→ adversary declared successful

De-Randomized Signatures Are Not Fault-Resilient

Sign_{dr}(sk, m)

$r \leftarrow \text{Hash}(sk, \langle m \rangle)$

$s \leftarrow \text{Sign}_r(sk, \langle m \rangle; r)$

return s

1. Query O_{Sign} on m
 - no faults
 - obtain signature s on m
2. Query O_{Sign} on m
 - first $\langle m \rangle$: do nothing
 - second $\langle m \rangle$: flip bit (to m')
 - obtain signature s on m'
3. Create new forgery due to re-used randomness r for signatures on m and m'

Combining Randomization & De-Randomization

Sign_c(sk, m)

$r' \leftarrow_{\$} \{0, 1\}^\lambda$

$r \leftarrow \text{Hash}(sk, \langle m \rangle, \langle r' \rangle)$

$s \leftarrow \text{Sign}_r(sk, \langle m \rangle; \langle r \rangle)$

return s

Combining security (provably)

- **de-randomization** for regular EUF-CMA security under bad randomness
- **randomization** for fault-resilient EUF-CMA security under differential faults on m, r, r'



Fault Resilience for Authenticated Encryption

A Similar Setting

- good randomness isn't always available
- nonce-based authenticated encryption (AE) to avoid randomness
- nonce-misuse resistance hedging against repeated nonces

- but what about faults?

SIV Mode of Operation: Synthetic IV [RS06]

Nonce-misuse resistance ...

$\text{Enc}_{\text{SIV}}((K_1, K_2), \mathbf{N}, \mathbf{A}, \mathbf{m})$

$\mathbf{IV} \leftarrow \text{PRF}(K_1, \langle \mathbf{N} \rangle \mid \langle \mathbf{A} \rangle \mid \langle \mathbf{m} \rangle)$

$c \leftarrow \text{Enc}(K_2, \langle \mathbf{m} \rangle; \langle \mathbf{IV} \rangle)$

return (IV, c)

... but vulnerable to faults

1. Query O_{Enc} on $(\mathbf{N}=00..0, \mathbf{A}, \mathbf{m})$
– no faults, obtain $c_1 = c$ or \$
2. Query O_{Enc} on $(\mathbf{N}=10..0, \mathbf{A}, \mathbf{m})$
– $\langle \mathbf{N} \rangle$ callback: flip 1st bit
– obtain $c_2 = c$ or *different* \$
3. Distinguish by checking if $c_1 = c_2$

SIV\$: Combining Randomization & De-Randomization

$\text{Enc}_{\text{SIV\$}}((K_1, K_2), \mathbf{N}, \mathbf{A}, \mathbf{m})$

$r \leftarrow_{\$} \{0, 1\}^\lambda$

$\mathbf{IV} \leftarrow \text{PRF}(K_1, \langle \mathbf{N} \rangle | \langle \mathbf{A} \rangle | \langle \mathbf{m} \rangle | \langle \mathbf{r} \rangle)$

$c \leftarrow \text{Enc}(K_2, \langle \mathbf{r} \rangle | \langle \mathbf{m} \rangle; \langle \mathbf{IV} \rangle)$

return (IV, c)

Combining security (provably)

- **synthetic IV approach** for nonce-misuse res. AE security under bad randomness
- **augmented randomness** for fault-resilient nm-res. AE security under diff. faults on N, A, m, r, IV

Fault-resilient AE mode
translating signature concepts

Summary

- Introduced **generic model** for understanding **fault resilience** in computational **security proofs**
- Signatures
 - confirm fault attacks on de-randomized signatures
 - provable security of **combined randomization + de-randomization**
- Authenticated encryption
 - fault-attack treatment of SIV mode of operation
 - propose **combined SIV\$ mode** achieving fault resilience



XEdDSA

Applying the Generic Fault Resilience Model

- Select your favorite crypto primitive
 - fault resilience model is generic
- Revise security definitions towards fault-resilient variant
 - What has to be taken care of when faults might happen in schemes?
- Augment scheme with faulting profile
 - different memory variables / algorithms may be differently vulnerable
- Assess provable fault-resilient security of augmented scheme

Summary

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Thank you!